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iSQI

CTAL-TTA-001

ISTQB Certified Tester Advanced Level - Technical Test Analyst (CTAL-TTA-Syll2012)



- A. A design review meeting
- B. An architect translating requirements into design
- C. Performing unit tests against design
- D. A test log showing code coverage achieved

Answer: A

QUESTION: 59

Part 7 "Test Process & Incident Management"

You are asked to provide practical and pragmatic testing solution for a commercial system where the main user interface is via the World Wide Web. It is critical that the company's existing good name and market profile are not damaged in any way. Time to market is not an issue when appropriate testing solutions are identified to mitigate business risks.

The following issues are currently provided as a risk:

- lack of usability requirements
- security during on-line transactions
- perceived performance of the system and response time from the user interface
- a required availability of almost 100%.

As a technical test analyst, which documents would you typically create? 1 credit
[K2]

- A. Test plan, test design, test cases and test log
- B. Test plan, test log, test design and test summary report
- C. Test script, test log, incident report and test design
- D. Incident report, test item transmittal report, test cases and test procedure

Answer: C

QUESTION: 60

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As a technical test analyst, you have found out during test case design that the design document is incomplete. Which is an example of a good way to communicate that problem in an email? 1 credit [K2]

- A. E-mail: "Until I have received an updated version of the design document, I will not do any work on the test design."
- B. E-mail: "When will it be possible to receive the missing information? Test design is impeded by a lack of clarity here."
- C. E-mail: "Here we go again. The developer gave us incomplete and ambiguous design specifications. Typical."
- D. Do not communicate the problem, just log the delaying effect of the information problem and be ready to explain the delays to the test manager later.

Answer: B

QUESTION: 61

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As a technical test analyst, you are involved in a risk analysis session using the Failure Mode and Effect Analysis technique. You are calculating risk priorities. Which of the following are the major factors in this exercise? 1 credit [K2]

- A. Severity and priority
- B. Functionality, reliability, usability, maintainability, efficiency and portability
- C. Likelihood and impact
- D. Financial damage, frequency of use and external visibility

Answer: C

QUESTION: 62

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In which of the following test documents would you expect to find the preconditions to start executing a set of test cases? 1 credit [K2]

- A. Level test plan
- B. Test procedure specification
- C. Test design specification
- D. Master test plan

Answer: B

QUESTION: 63

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Defects are discovered through test analysis and design because... 1 credit [K2]

- A. the tasks involve extracting information from the test basis
- B. developers are involved in writing test cases
- C. the cost of fixing a defect will increase if found later on
- D. the author of the test basis will have made errors

Answer: A

QUESTION: 64

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The development manager asks you to identify suitable test coverage entry criteria for a component test. Which TWO of the following would you recommend as appropriate for entry criteria to a component testing phase? 2 credits [K3]

- A. 100% statement coverage
- B. No critical outstanding defects
- C. Test log available
- D. Code review completed
- E. Static analysis shows no major violations

Answer: D, E

QUESTION: 65

A test team is keeping logs of the incidents that they observe. They use a commonly-available word processor to write the incident reports in individual file, which are held in shared storage. They track the incidents along with some incident details, in a spreadsheet.

Identify the most significant risk introduced by this approach to incident management. 3 credits [K4]

- A. Excel list may not be insynchronizationwith Word documents
- B. Low level of usability
- C. Spreadsheet/text files may not be complete
- D. Does not map to IEEE 1044

Answer: A



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